

Fig. 1 Prior art

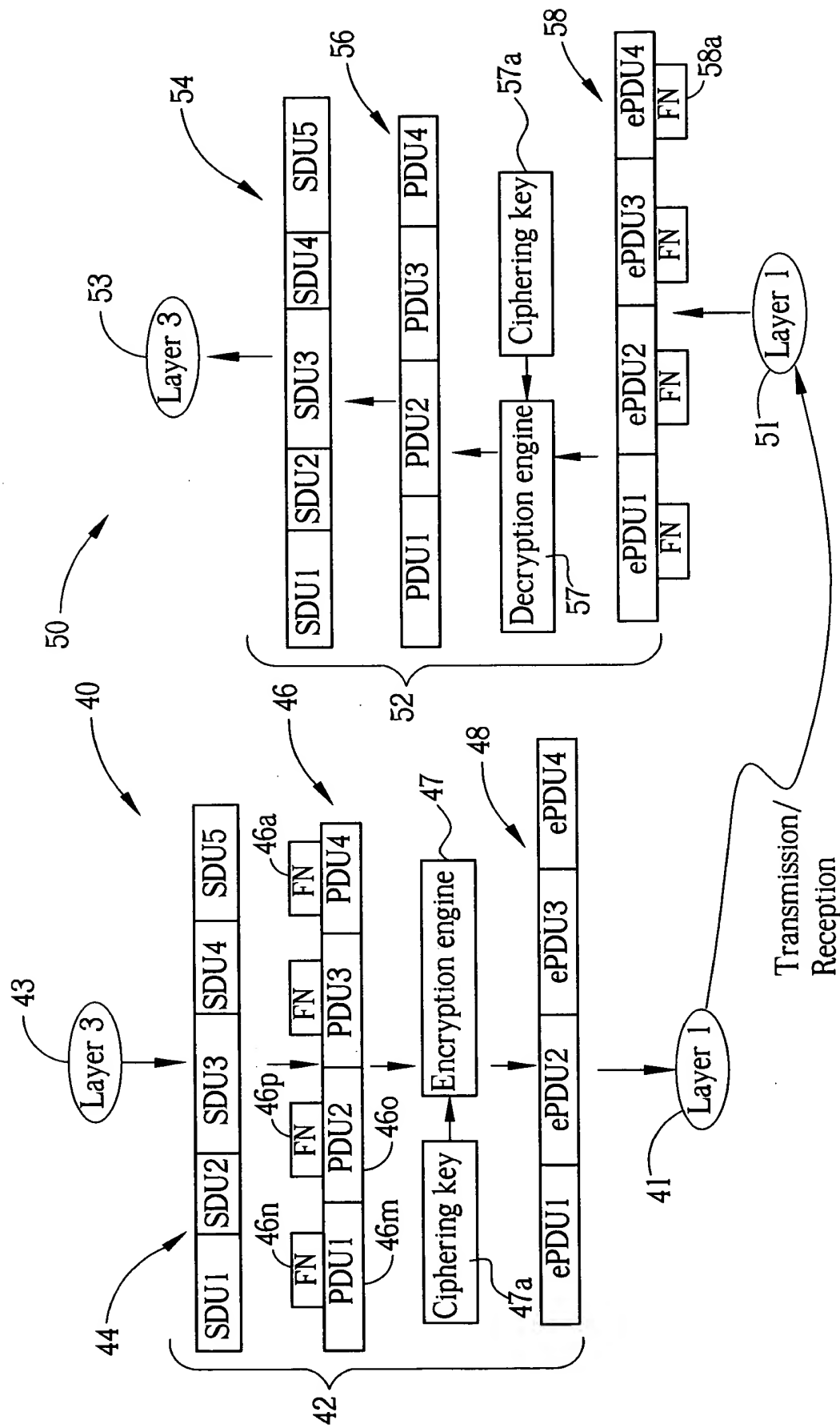


Fig. 2 Prior art

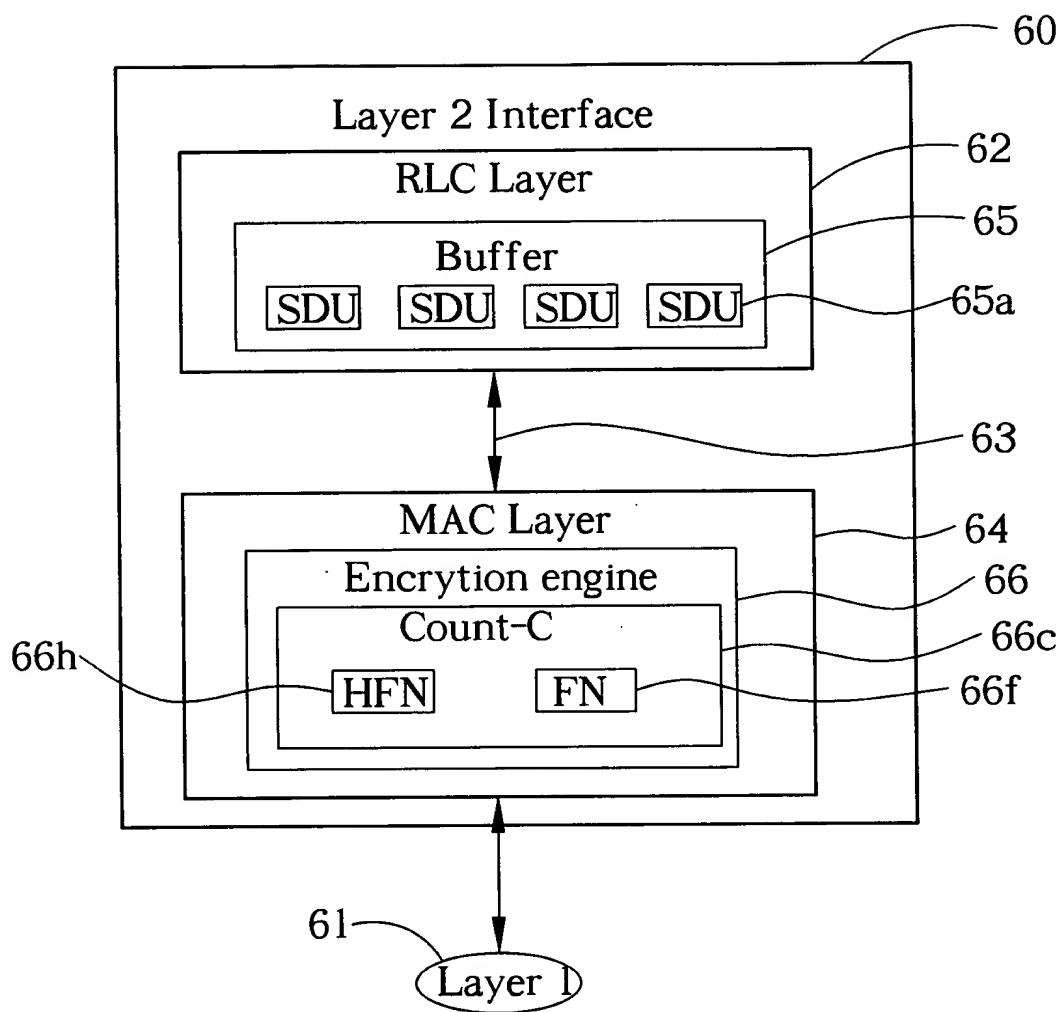


Fig. 3 Prior art

FIG. 4 is a schematic diagram of a prior art system for processing a batch of data. The system includes a batch of data 70, which is divided into a plurality of data blocks 74. Each data block 74 is processed by a processor 72, which generates a result 75. The results 75 are then stored in a storage device 76. The storage device 76 is connected to a computer system 77, which is used to analyze the results 75.

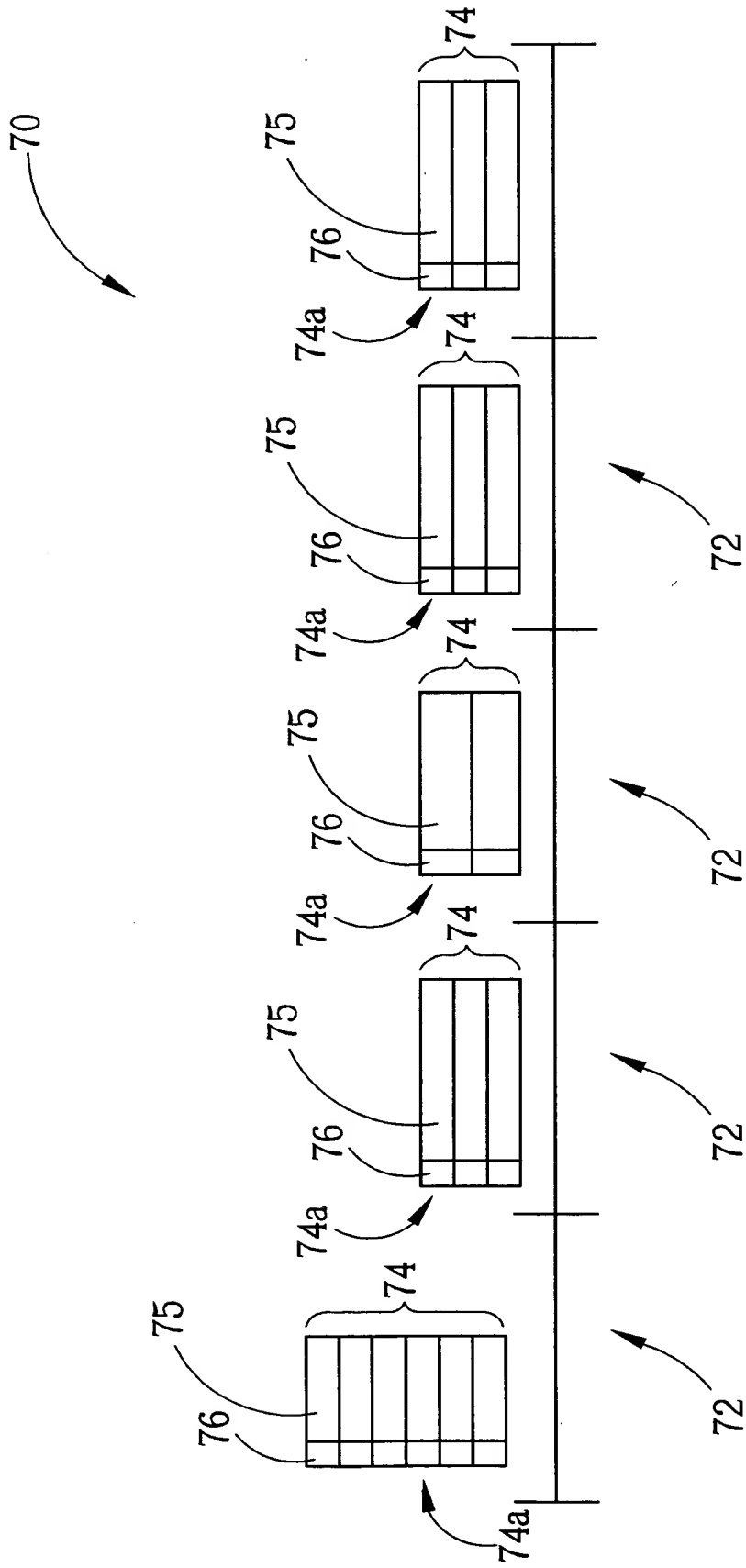


Fig. 4 Prior art

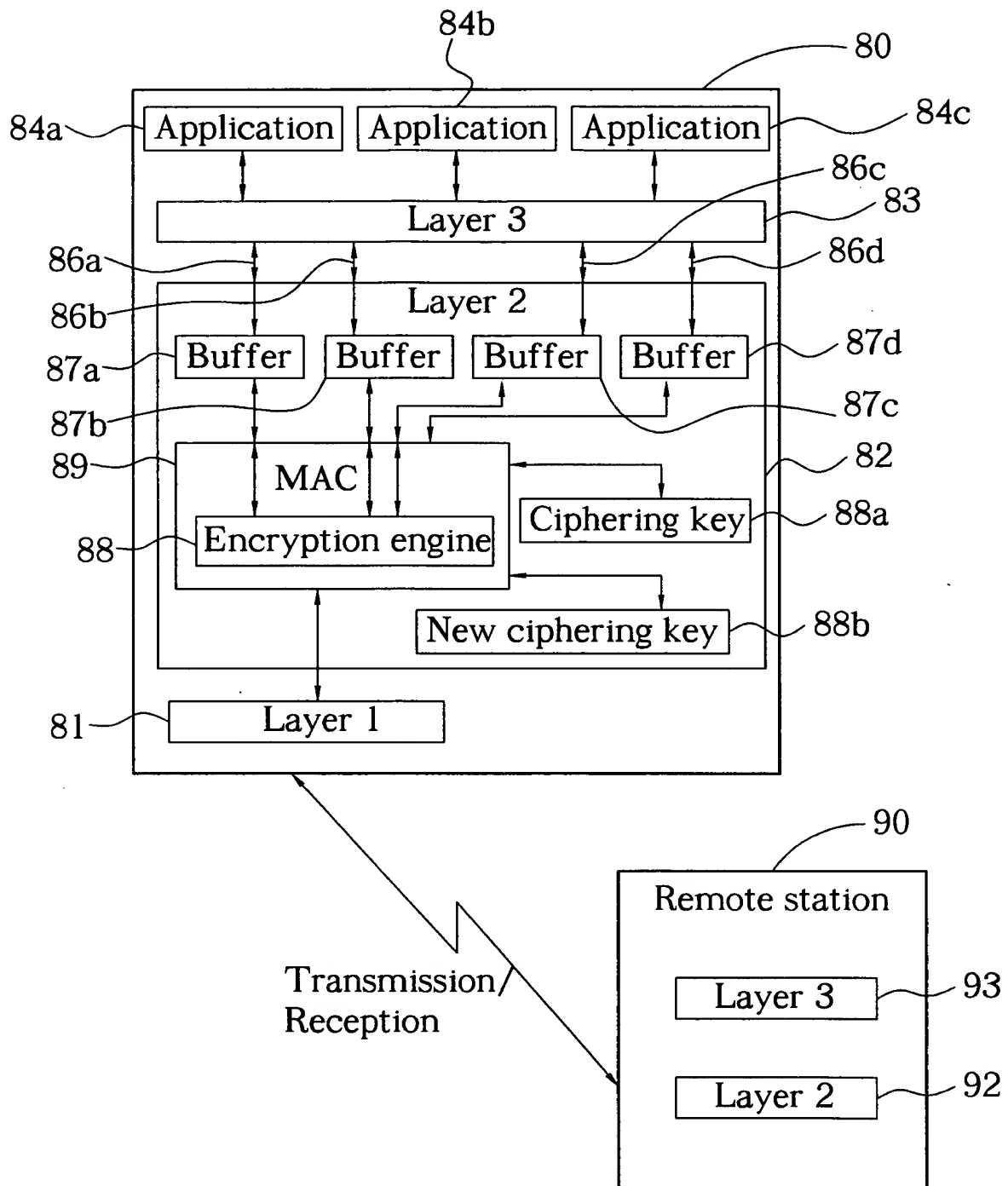


Fig. 5 Prior art

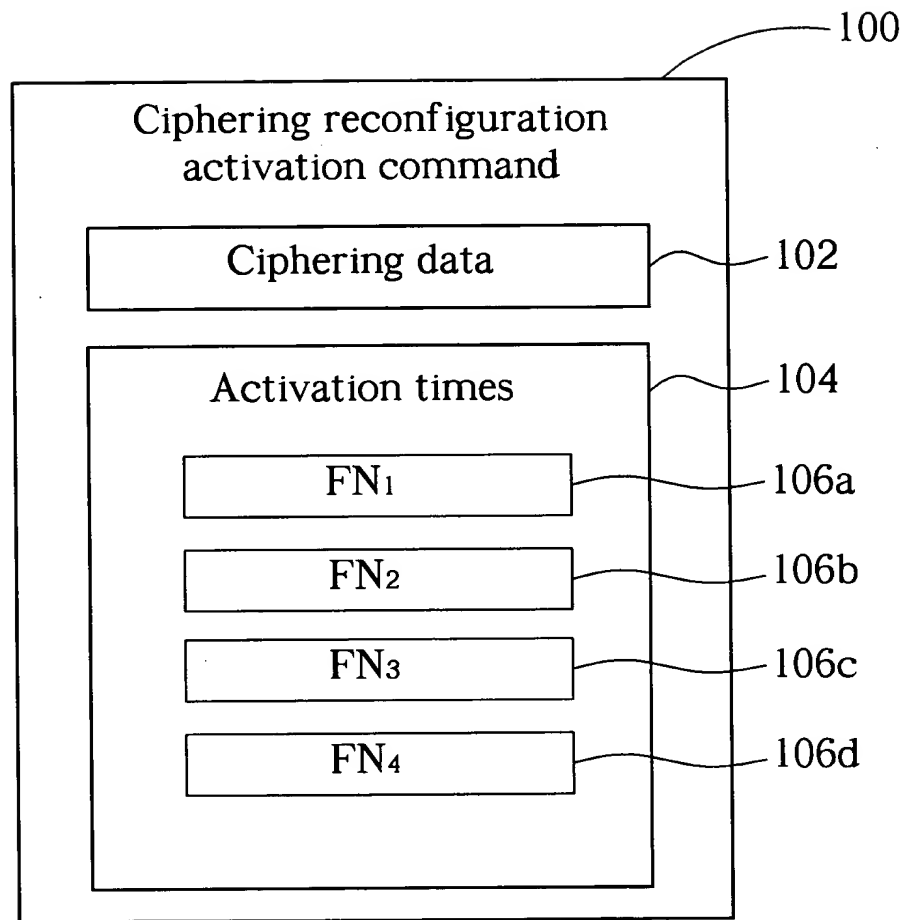


Fig. 6 Prior art

FIG. 7

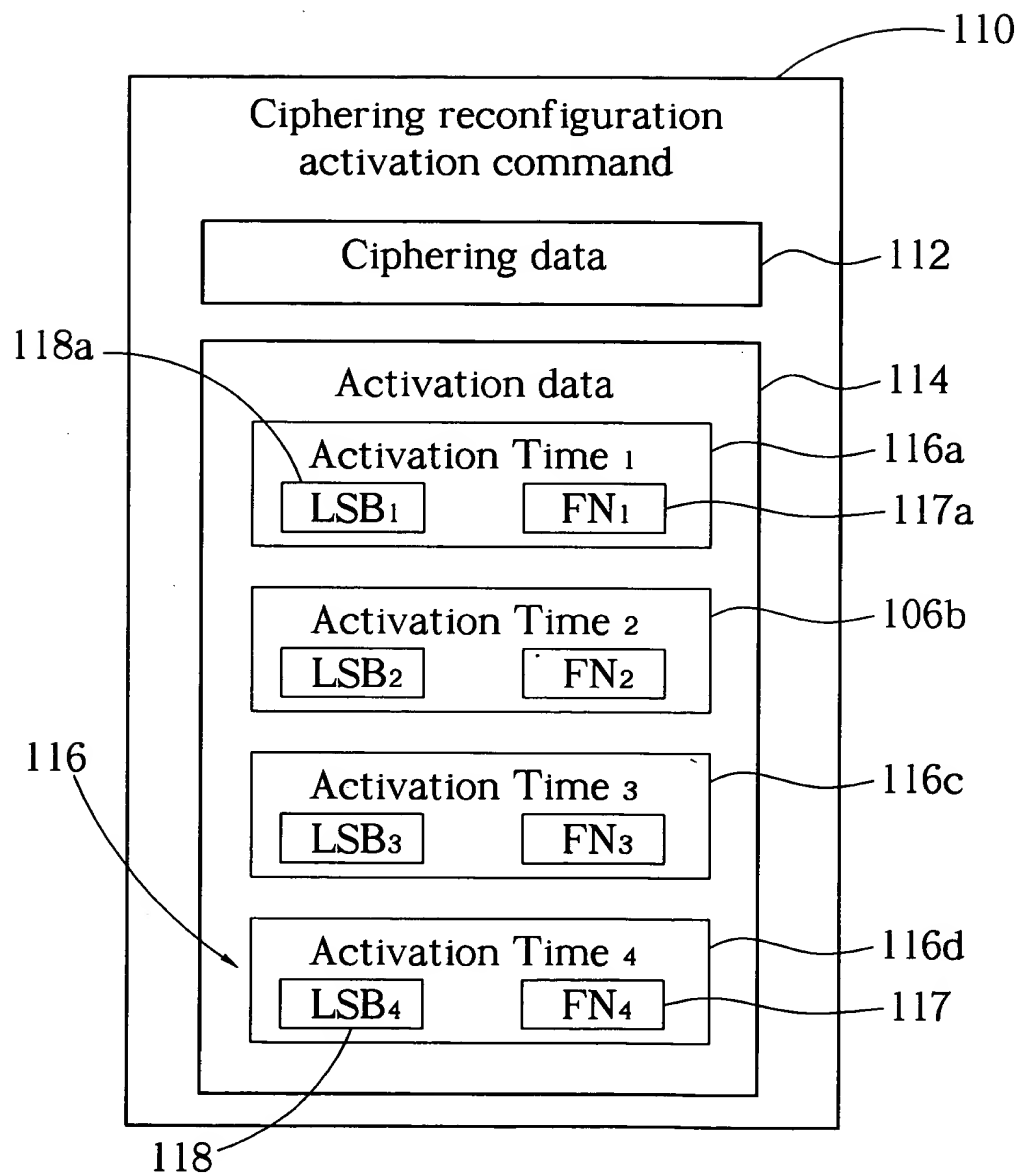
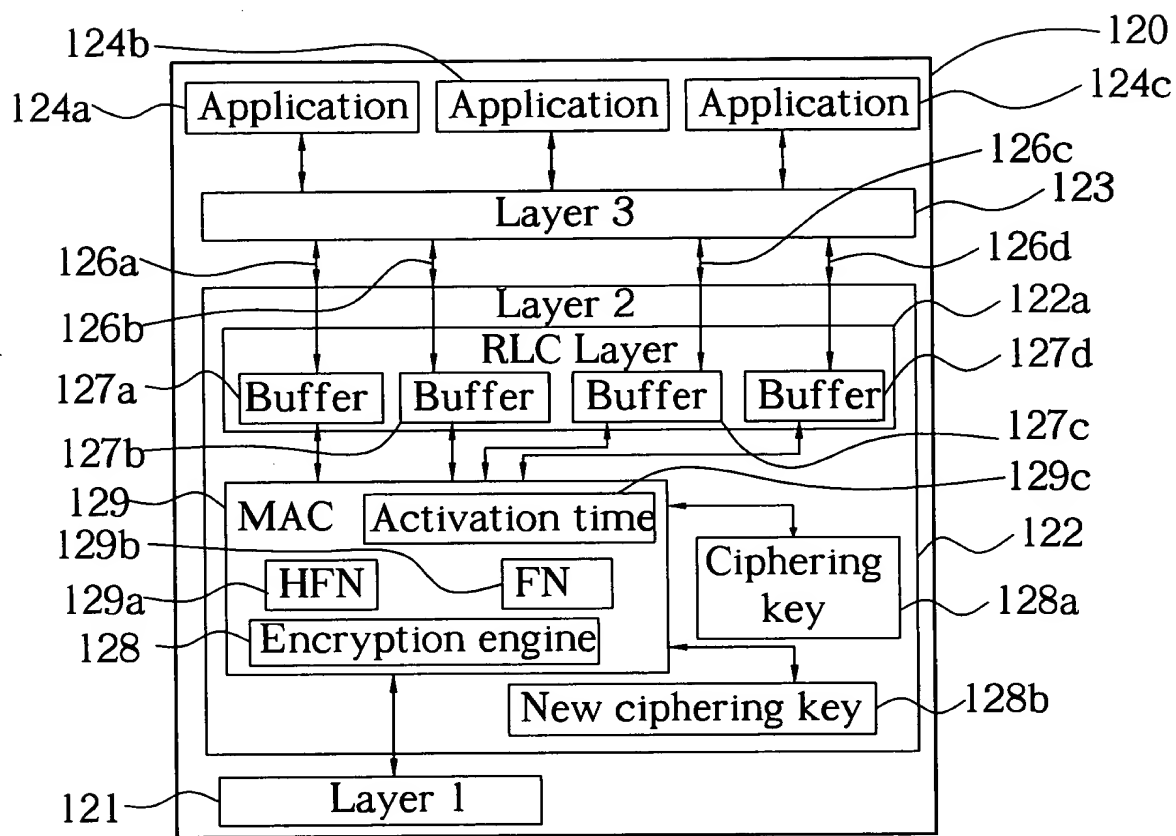


Fig. 7



Transmission/
Reception

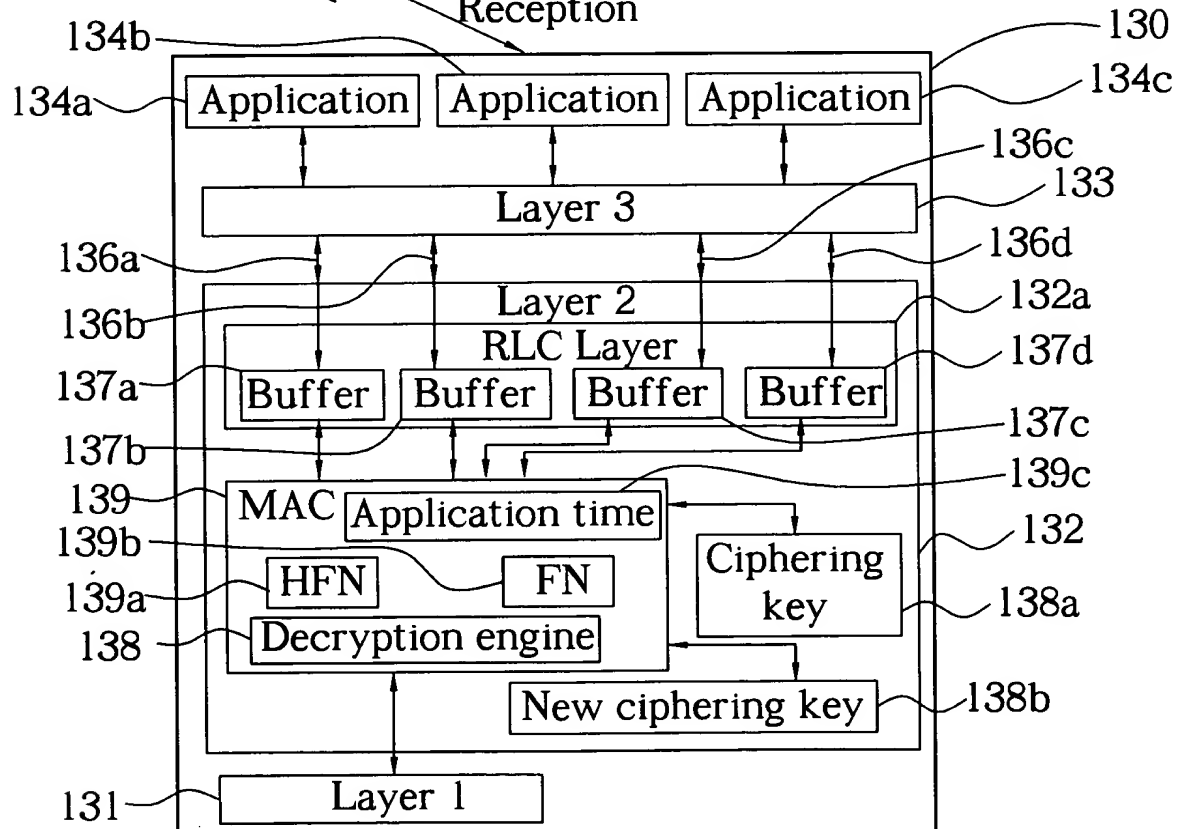


Fig. 8

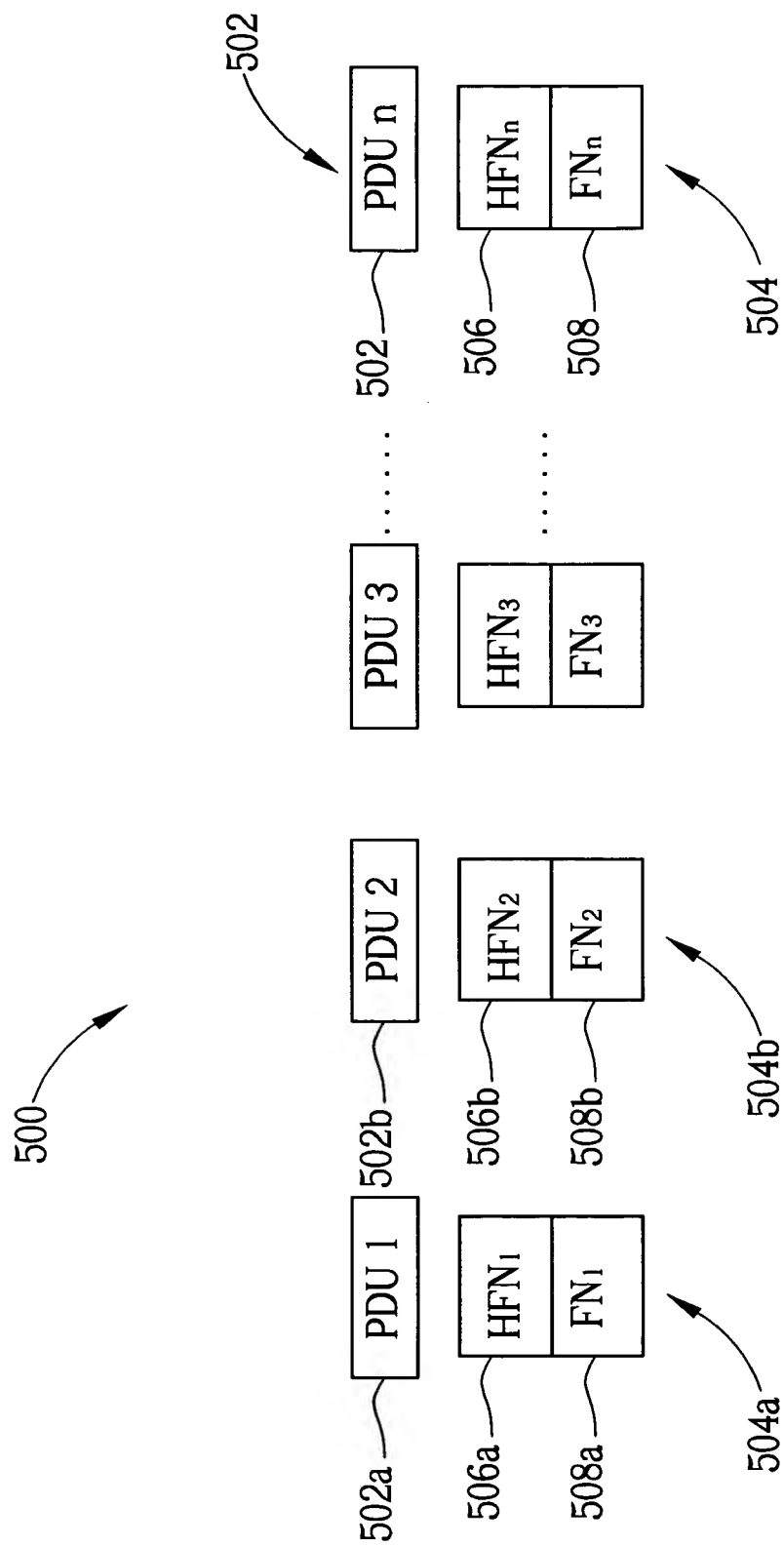


Fig. 9

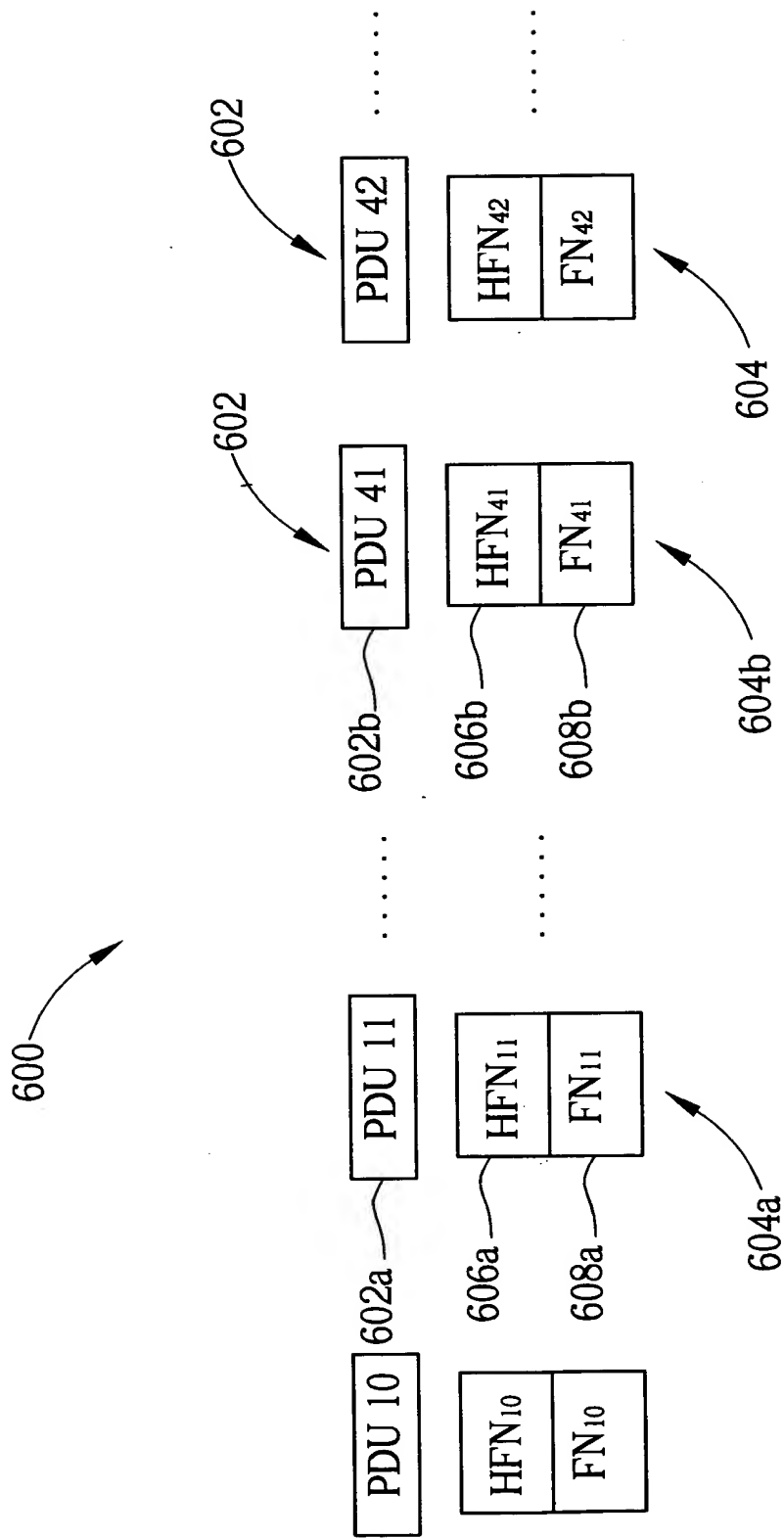


Fig. 10